

AMENDMENTS TO THE DRAWINGS

The attached drawing sheet is a revised formal version of FIG. 9. This sheet replaces the original sheet containing FIG. 9.

Attachment: 1 Replacement Sheet (FIG. 9)

The circuit diagram shows a differential amplifier configuration. A current source CS1 (labeled 7, 8) is connected between a supply voltage Vb (labeled 5) and a node between resistors R2 and R3. The output of the current source is indicated by a dashed line and an arrow. The circuit includes several resistors: R1, R4, R5, R6, R7, and R8. Transistors T1, T2, T3, T4, T5, and T6 are shown. The input signal Sa (labeled 2) is applied to the base of T3. The output Vo (labeled 3) is taken from the collector of T1. The circuit is connected to ground (labeled 6). A label 1 with an arrow points to the top rail of the circuit.